

WEST Search History

Hide Items

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DATE: Friday, April 01, 2005

09/ 895,469

Hide?	Set Name	Query	Hit Count
	DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR		
<input type="checkbox"/>	L50	L45 and L49	1
<input type="checkbox"/>	L49	L38 and L48	7
<input type="checkbox"/>	L48	BIOS and L47	11
<input type="checkbox"/>	L47	PCI adj configuration adj information	16
<input type="checkbox"/>	L46	PCI adj configuration adj space adj accesser	1
<input type="checkbox"/>	L45	L43 and L44	6
<input type="checkbox"/>	L44	translat\$3 and compar\$3	210017
<input type="checkbox"/>	L43	L41 and L42	6
<input type="checkbox"/>	L42	resource adj descriptor\$1	130
<input type="checkbox"/>	L41	L39 and L40	10
<input type="checkbox"/>	L40	device adj driver\$1	23182
<input type="checkbox"/>	L39	L37 and L38	12
<input type="checkbox"/>	L38	(plug adj2 play) and Windows	2051
<input type="checkbox"/>	L37	registry same key\$1 same subkey\$1	105
<input type="checkbox"/>	L36	BIOS same configuration	4429
<input type="checkbox"/>	L35	L31 and L34	9
<input type="checkbox"/>	L34	L30 and L31	9
<input type="checkbox"/>	L33	L31 and L32	3
<input type="checkbox"/>	L32	(plug adj2 play) near\$5 device\$1	1373
<input type="checkbox"/>	L31	L23 and L30	9
<input type="checkbox"/>	L30	pci and usb	7009
<input type="checkbox"/>	L29	L23 and L26	6
<input type="checkbox"/>	L28	L16 and L27	3
<input type="checkbox"/>	L27	L12 and L26	618
<input type="checkbox"/>	L26	pci same usb	3506
<input type="checkbox"/>	L25	L8 and L24	0
<input type="checkbox"/>	L24	L10 and L23	8
<input type="checkbox"/>	L23	719/327	71
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<input type="checkbox"/>	L21	(PCI or USB) and L19	23
<input type="checkbox"/>	L20	PCI and L19	16

INVENTOR
NAME SEARCH

done on

Apr 4-1-2005
ST. JL ✓COPYENDING
US Pat. 6,711,630
checked for
double patenting.

OK ✓

ST. JL

4-1-2005

<input checked="" type="checkbox"/>	L19	L16 and compar\$3 and memory	54
<input type="checkbox"/>	L18	L8 and L16	0
<input type="checkbox"/>	L17	L8 same L16	0
<input type="checkbox"/>	L16	device adj driver adj configuration	140
<input type="checkbox"/>	L15	L8 and L13	7
<input type="checkbox"/>	L14	L9 and L13	0
<input type="checkbox"/>	L13	L8 and L10 and L12	7
<input type="checkbox"/>	L12	L1 or L2 or L11	21796
		(710/1 710/2 710/3 710/4 710/5 710/6 710/7 710/8 710/9 710/10 710/11 710/12 710/13 710/14 710/15 710/16 710/17 710/18 710/19 710/20 710/21 710/22 710/23 710/24 710/25 710/26 710/27 710/28 710/29 710/30 710/31 710/32 710/33 710/34 710/35 710/36 710/37 710/38 710/39 710/40 710/41 710/42 710/43 710/44 710/45 710/46 710/47 710/48 710/49 710/50 710/51 710/52 710/53 710/54 710/55 710/56 710/57 710/58 710/59 710/60 710/61 710/62 710/63 710/64 710/65 710/66 710/67 710/68 710/69 710/70 710/71 710/72 710/73 710/74 710/100 710/300 710/301 710/302 710/303 710/304 710/104 710/105 710/106 710/107 710/108 710/109 710/110 710/111 710/112 710/113 710/114 710/115 710/116 710/117 710/118 710/119 710/120 710/121 710/122 710/123 710/124 710/125 710/305 710/306 710/307 710/308 710/309 710/310 710/311 710/312 710/313 710/314 710/315 710/316 710/317).ccls.	
<input type="checkbox"/>	L11		20005
<input type="checkbox"/>	L10	PCI and (device adj driver\$1)	3385
<input type="checkbox"/>	L9	L7 and L8	4
<input type="checkbox"/>	L8	compar\$3 same (memory adj location\$1)	5849
<input type="checkbox"/>	L7	L5 and L6	408
<input type="checkbox"/>	L6	L1 or L2	2480
<input type="checkbox"/>	L5	L3 and L4	12172
<input type="checkbox"/>	L4	configuration same L3	12172
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<input type="checkbox"/>	L3	device\$1 same bus\$2	98258
<input type="checkbox"/>	L2	(719/321 719/322 719/323 719/324 719/325 719/326 719/327).ccls.	435
<input type="checkbox"/>	L1	(713/1 713/2 713/100).ccls.	2101

END OF SEARCH HISTORY